

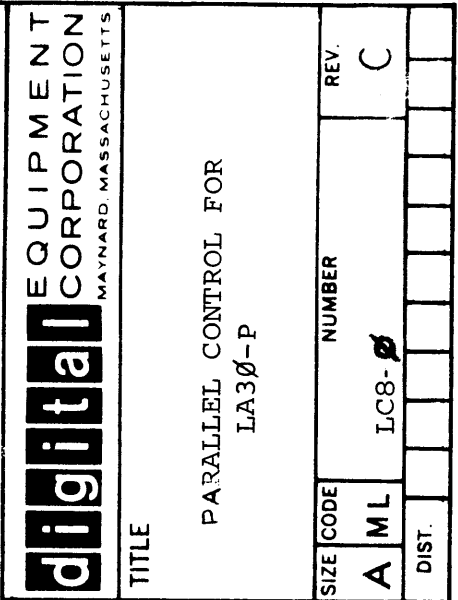
**LC8-E
DECwriter control
engineering drawings**

MASTER DRAWING LIST

MAINTENANCE MANUALS		UNIT VARIATIONS											
NO.	TITLE	LC8-E	LC8-L	LC8-P	LC8-R	LC8-S	LC8-T	LC8-U	LC8-V	LC8-W	LC8-X	LC8-Y	LC8-Z
	LC8-E PARALLEL CONT	X	-										
	LC8-L PARALLEL CONT	-	X										

USED ON OPTIONS	
PDP8-E	
PDP 8-L	
PDP8-F	
PDP8-M	
PDP8-I	
PDP12	

APPROV. <i>[Signature]</i>	DATE 10/71	CHG. NO. LC8-1	DRN. K. GULICK	DATE 7-26-71	TITLE
6/72 LC8-3	4/72 LC8-2	10/71 LC8-1	CHK'D. K. GULICK	DATE 7-26-71	PARALLEL CONTROL FOR LA30-P
			ENG. <i>[Signature]</i>	DATE 7-30-71	
			PROJ. ENG. <i>[Signature]</i>	DATE 7-30-71	
			PROD. <i>[Signature]</i>	DATE 7-30-71	
			FIRST USED ON PDP8-E		
			SCALE NONE		
			SHEET 1 OF 2		
			SIZE CODE A ML	NUMBER LC8-0	REV. C

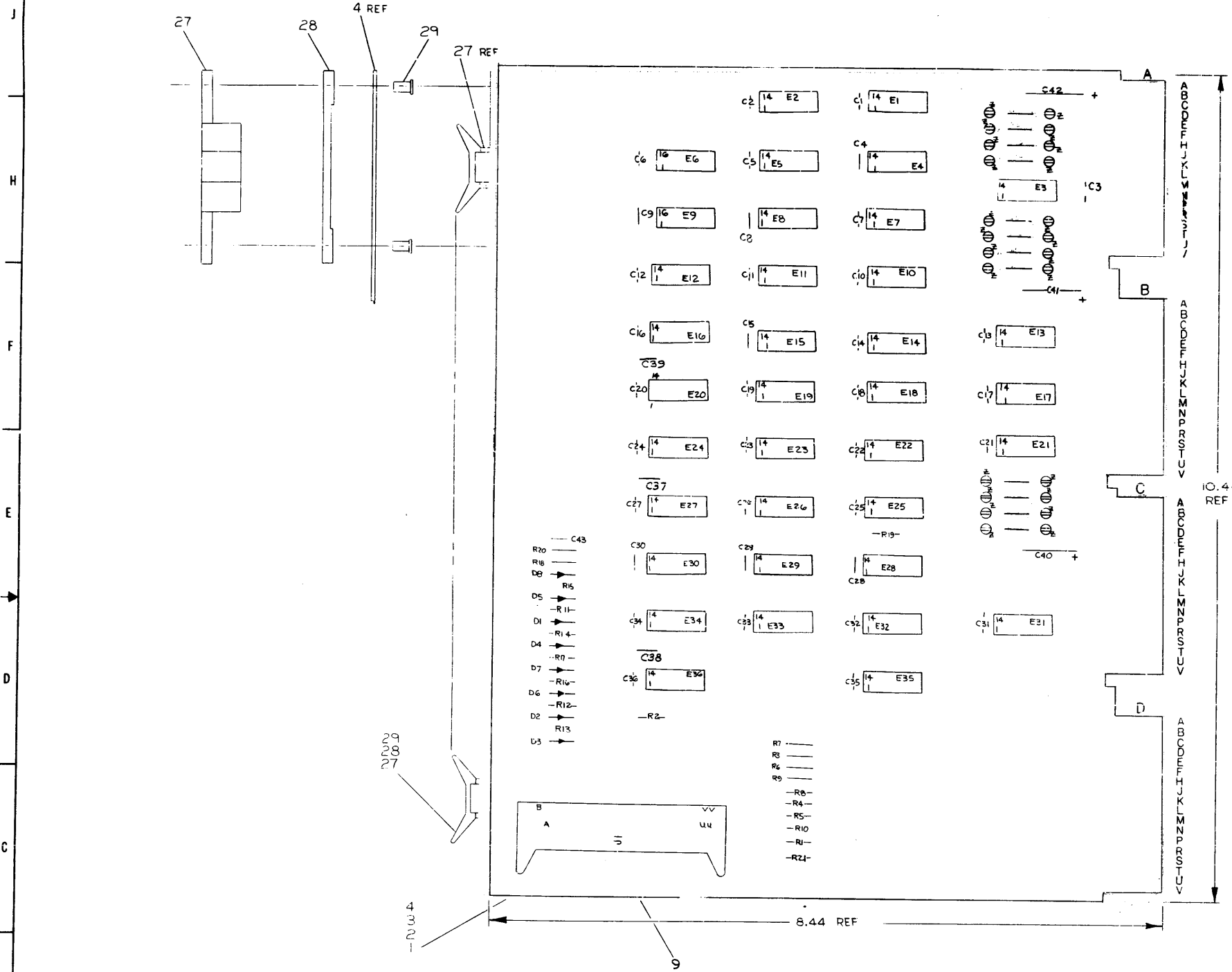


PRINT SET		DWG. NO.	REV. NO. OF LET. SHEETS	TITLE	OPTION NO.
X	LC8-E	E-CS-M8329-0-1	# 2	PARALLEL INTERFACE	
X	LC8-L	D-IA-7008417-0-0	1	INTERFACE CABLE	LC11
X	LC8-P	A-SP-LC8-E-1	10	ENGINEERING SPECIFICATION	
-	LC8-R	A-SP-LC8-E-2	A 2	ACCEPTANCE PROCEDURE	
-	LC8-S	A -SP-LC8-E-3	2	TEST PROCEDURE	
X	LC8-T	LIBKIT-8E-LC8E	REF	SOFTWARE KIT LIST	
-	LC8-U	D-CS- M7718-0-1	# 1	KEYBOARD INTERFACE	
-	LC8-V	C-CS- M7719-0-1	# 1	TRANSMIT DATA	
-	LC8-W	D-IA- 7008514-0-0	1	INTERFACE CABLE LC8-L	
-	LC8-X	A-SP-LC8-L-1	5	ENGINEERING SPECIFICATION	
-	LC8-Y	A-SP-LC8-L-2	2	TEST PROCEDURE	
X	LC8-Z	A-SP-LC8-L-3	3	ACCEPTANCE PROCEDURE	
X	LC8-0	A-AL-LC8-0-1	1	ACCESSORY LIST	

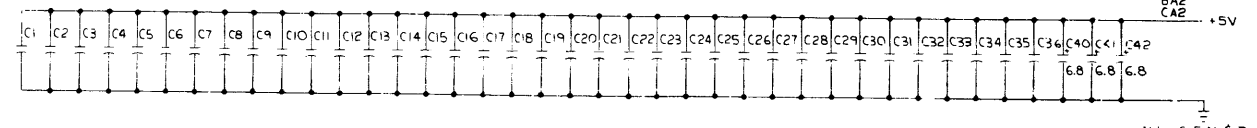
TITLE	PARALLEL CONTROL FOR LA30-P	SHEET 2 OF 2	SIZE CODE A ML	NUMBER LC8-0	REV. C
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1. UNLESS OTHERWISE SPECIFIED:
RESISTORS ARE 220, 1/4W, 5%
CAPACITORS ARE .01UF, 100V, 10%

NOTES:
1. UNLESS OTHERWISE SPECIFIED:
RESISTORS ARE 220, 1/4W, 5%
CAPACITORS ARE .01UF, 100V, 10%



IC TYPE	QTY	REF	DESCRIPTION
DEC 5314	1	B	IC
DEC 5380	1	B	IC
DEC 5384	1	B	IC
DEC 7442	8	16	IC



ALL C, F, N & T PINS EXCEPT AC1

QTY	REF DESIGNATION	DESCRIPTION	PART NO.
30	30	WIRE #22 AWG SOLID BUE	107560-01
29	29	EYELET GS4-11 STAMPSON	100750
28	28	SPACER (CABLE CLAMP)	100704
27	27	HANDLE, FLIP-CHIP MAGENTA	900337-06
26	26	IC DEC 5384	100394
25	25	IC DEC 5380	100392
24	24	IC DEC 5314	100391
23	23	IC DEC 7442	100394
22	22	IC DEC 747	100429
21	21	IC DEC 97401	100473
20	20	IC DEC 7404	100368
19	19	IC DEC 7400	1009056
18	18	IC DEC 7402	1004004
17	17	IC DEC 7450	100580
16	16	IC DEC 7440	100599
15	15	IC DEC 7400	1005515
14	14	IC DEC 7474	1005547
13	13	RES. 750, 1/4W, 5%	301401
12	12	RES. 330, 1/4W, 5%	300365
11	11	RES. 220, 1/4W, 5%	300245
10	10	RES. 220, 1/4W, 5%	1000271
9	9	CONN, RIGHT ANGLE HEADER	1209941
8	8	DIODE D664	1100114
7	7	CAP. .01UF, 100V, 10% DISC	1000610
6	6	CAP. 6.8UF, 35V, 20% TANT	1000067
5	5	CAP. 470PF, 100V, 5% DM	1000024
4	4	ETCHED CIRCUIT BOARD	5004627
3	3	MODULE ECO HISTORY	5MH-M8329-B-6
2	2	ASSY/DRILLING HOLE LAYOUT	5AH-M8329-B-5
1	1	X-Y COORDINATE HOLE LOC.	KCO-M8329-B-4

ETCH BOARD REV C

PARALLEL INTERFACE

DEC NO. D664 EIA NO. IN 2606 DEC NO. EIA NO. A-PL-LC8-E-0

SEMICONDUCTOR CONVERSION CHART

EQUIPMENT CORPORATION

PARALLEL INTERFACE

DEC NO. D664 EIA NO. IN 2606 DEC NO. EIA NO. A-PL-LC8-E-0

SCALE 2/1

SHEET 1 OF 2

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0-0-718008417-0-0

WIRED TABLE

ITEM NO.	DESCRIPTION	FROM	TO	CUT POINT	
6	22 TWP	GRN	P1-E	P2-25	1
		WHT	P1-F	P2-17	2
		BLK	P1-H	P2-23	3
		BLU	P1-J	P2-16	4
		BLU	P1-K	P2-26	5
		RED	P1-L	P2-22	6
		GRN	P1-M	P2-24	7
		YEL	P1-N	P2-5	8
		BRN	P1-P	P2-4	9
		RED	P1-R	P2-18	10
		ORN	P1-S	P2-23	11
		WHT	P1-T	P2-15	12
		RED	P1-U	P2-25	13
		ORN	P1-V	P2-20	14
		BLK	P1-W	P2-3	15
		ORN	P1-X	P2-13	16
		YEL	P1-Y	P2-2	17
6	TWP	BLU	P1-Z	P2-7	18

WIRED TABLE

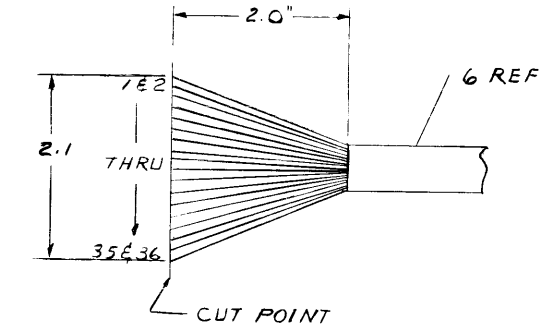
ITEM NO.	DESCRIPTION	FROM	TO	CUT POINT	
6	22 TWP	BLK	P1-AA	P2-3	19
		BRN	P1-BB	P2-10	20
		ORN	P1-CC	P2-1	21
		YEL	P1-DD	P2-6	22
		BLK	P1-EE	P2-4	23
		GRY	P1-FF	P2-9	24
		BLK	P1-GG	P2-4	25
		GRN	P1-JJ	P2-12	26
		BRN	P1-KK	P2-24	27
		WHT	P1-LL	P2-11	28
		GRY	P1-MM	P2-23	29
		RED	P1-NN	P2-14	30
		GRY	P1-PP	P2-2	31
		WHT	P1-RR	P2-8	32
		GRN	P1-SS	P2-1	33
		RED	P1-TT	P2-19	34
		WHT	P1-UU	P2-26	35
6	TWP	BLU	P1-VV	P2-21	36

LEGEND

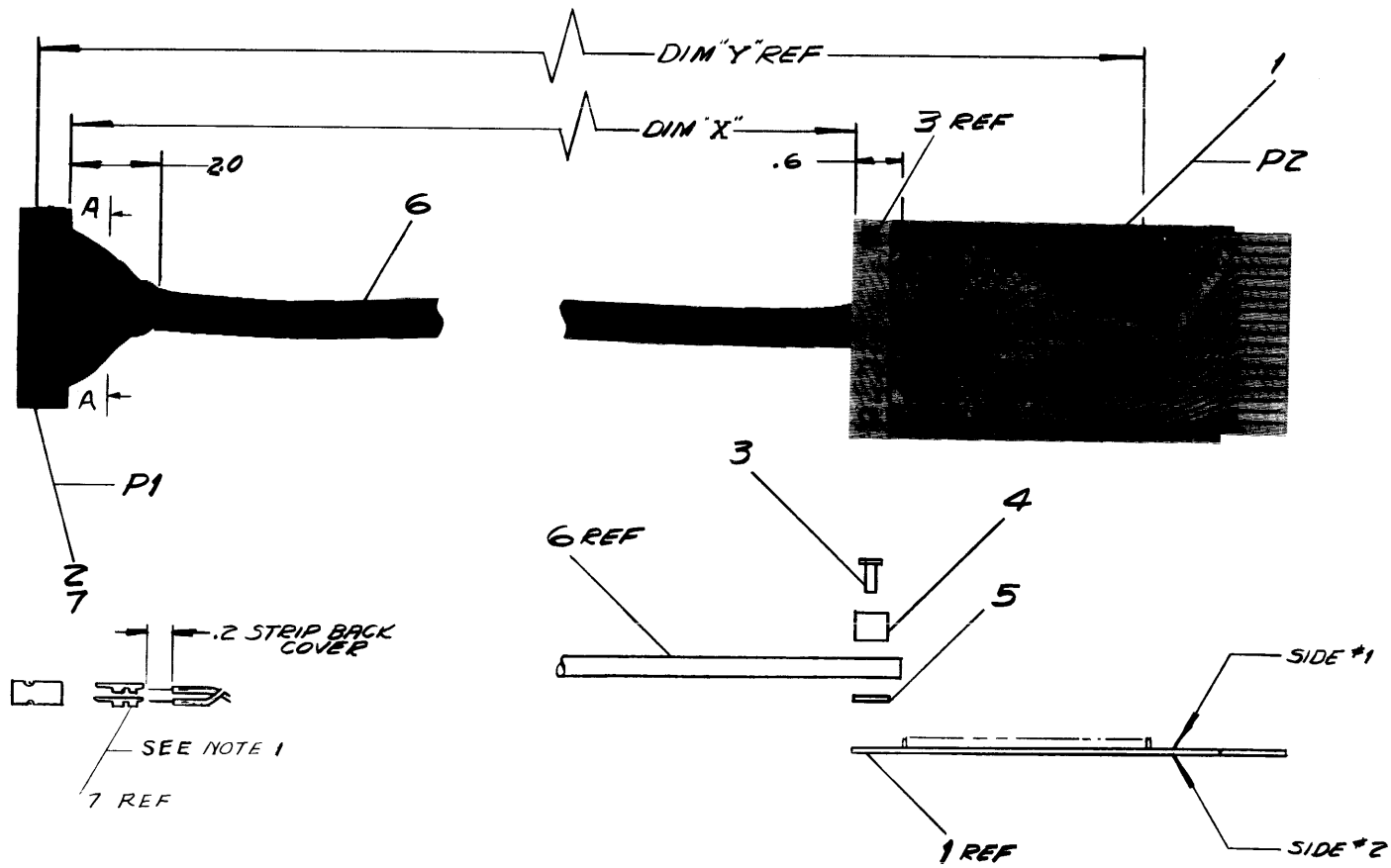
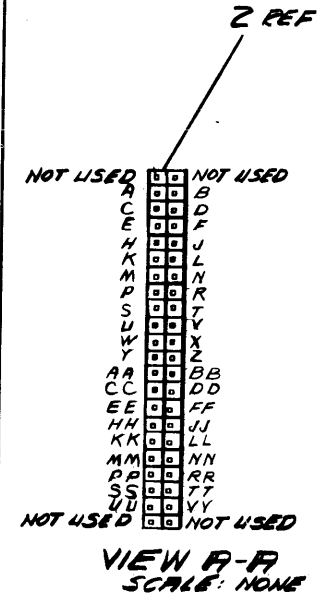
NUMBER	DIM "X" VARIATION	DIM "Y" (PRECUT) REF
7008417-25	25' ± 3"	25' 7"

NOTES:

- BERG # HT66-TOOL FOR CRIMPING PINS.
- MANUFACTURING SHOULD USE MACHINE CRIMPER.
- FOR INFORMATION ON BERG PINS REFER TO SPEC # 12-10089-0-0.



CUT POINT VIEW
SCALE: 1/1



QTY.	DESCRIPTION	PART NO.	ITEM NO.
36	CRIMP SOCKET BERG 47184	1210089-5	7
1	CABLE 36 COND.	9107700	6
1	TAPE #432 1/2 W 3M CA.	9007834	5
1	CLAMP CABLE	1202790	4
2	EYELET A-94 STIMP	9006741	3
1	HOUSING #20383 BERG	1210090-0	2
1	MODULE BO. M944	M944	1

FIRST USED ON OPTION/MODEL	LC11	QTY.	DATE	DATE	DATE	DATE
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		DECIMALS	ANGLES	PARTS LIST		
.XXX = .005	± 0° 30'			digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS		
.XX = .02				TITLE		
.X = .1				INTERFACE CABLE (LC11)		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				MATERIAL		
				NEXT HIGHER ASSY.		
				SCALE		
				SHEET		
				SIZE CODE		
				NUMBER		
				REV.		
				DIST.		

REV.	CHANGE NO.	BY	DATE
A	1	BEATTY	7-19-71

REV. A
NUMBER
DIA 7008417-0-0

ENGINEERING SPECIFICATION

digital

CONTINUATION SHEET

TITLE LC8-E (M8329)

- 9. The printer cannot print lower case characters. These are interpreted as upper case.
- 10. There is no BELL; CNTRL G is treated as non-printing.
- 11. End of line (> 80 characters) is trapped and any subsequent characters sent before a CAR RET are not printed.

II. Physical

The LC8-E is built on a single quad size board which plugs directly into the OMNIBUS. The etched board number is M8329.

III. Specifications - Environment

Operating temperature: 0 to 55 degrees C
 Operating humidity: 10 to 90% non-condensing
 During storage, temperature extremes of -15 to +65 degrees C can be tolerated.

IV. Specifications - Operating

- A. Type of transmission: Parallel
 Type of Reception: Parallel
- B. Control Signals:
 - 1. Receiver Strobe: Sent to the LA30 teleprinter to enable the printing of a character. It is sent at Time State One (TS1) of the instruction following the Input/Output transfer instruction. See the timing diagram in part D of this section.
 - 2. Demand: Received from the LA30 printer requesting the next character from the PDP8/E. See the timing diagram in part D of this section.
 - 3. Transmitter Strobe: Received from the LA30 keyboard; used to clock the character into the input buffer.

SIZE A	CODE SP	NUMBER LC8-E-1	REV
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ENGINEERING SPECIFICATION

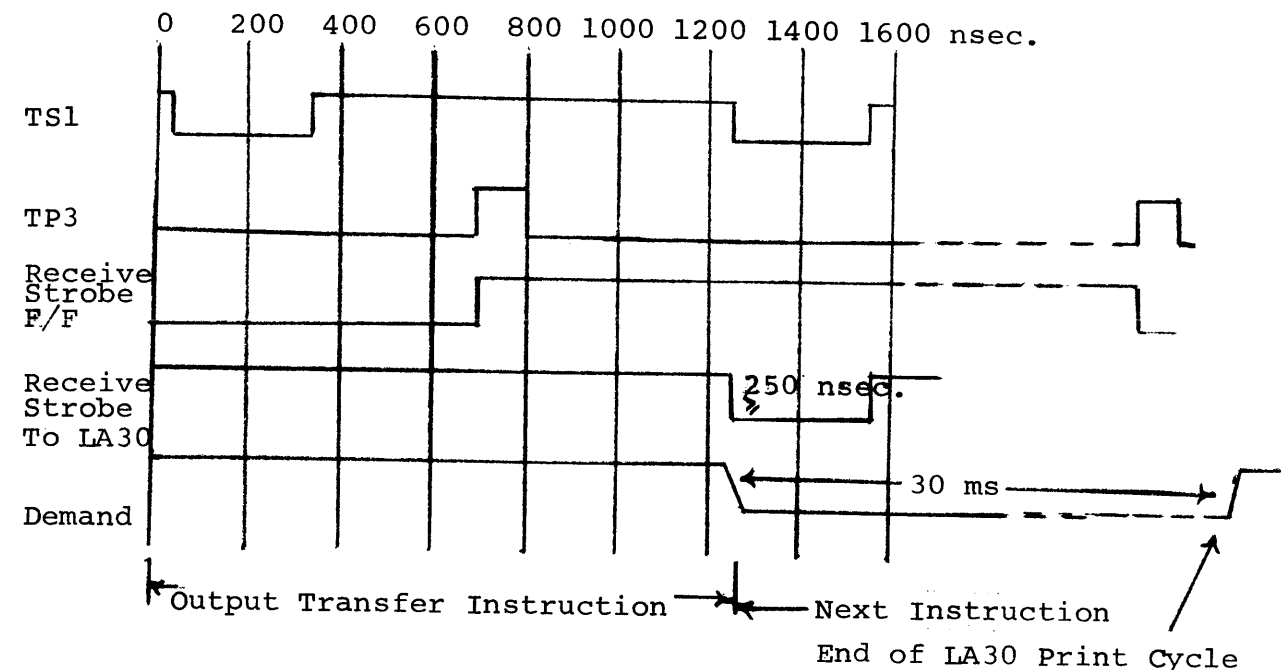
digital

CONTINUATION SHEET

TITLE LC8-E (M8329)

C. Number of Data Elements per character: Seven.

D. Timing Diagram:



E. Cable

The standard cable length is 25 feet. The cable used is a DEC #7008417.

V. Programming

The LC8-E uses the standard PDP8/E teletype keyboard/teletype teleprinter instruction set with the following changes:

LA30 Keyboard Instructions

- *6XXØ KCF Clear Keyboard Flag
Clears the keyboard flag only
- 6XX1 KSF Skip on Keyboard Flag
Skip next sequential instruction if keyboard flag is set.

SIZE A	CODE SP	NUMBER LC8-E-1	REV
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ENGINEERING SPECIFICATION



CONTINUATION SHEET

TITLE LC8-E (M8329)

- 6XX2 KCC Clear Keyboard Flag
Clear the keyboard flag and the AC only.
- 6XX4 KRS Read Keyboard Buffer Static
OR's the contents of the input buffer with AC5 through 11 and leaves the result in AC5-11. AC4 is set. Neither the AC nor the keyboard flag is cleared.
- **6XX5 KIE Set/Clear Interrupt Enable
Set or clear the interrupt enable flip-flop as determined by AC11.
- 6XX6 KRB Read Keyboard Buffer
Clear the keyboard flag and the AC. Transfer the contents of the input buffer to AC5 through AC11. Set AC4.

LA30 Teleprinter Instructions

- 6YY0 TFL Set Teleprinter Flag
Set the teleprinter flag
- 6YY1 TSF Skip on Teleprinter Flag
Skip the next sequential instruction if the teleprinter flag is set.
- *6YY2 TCF Clear Teleprinter Flag
Clears the teleprinter flag.
- 6YY4 TPC Load Teleprinter Buffer and Print
Transfer AC bits 5 through 11 to the output buffer and set Receiver Strobe. At Time State One (TS1) of the following instruction, the character is transferred to the LA30 teleprinter. The teleprinter flag is not cleared.
- 6YY5 SPI Skip if Teletype Interrupt
Skip next sequential instruction if keyboard or teleprinter flag is set and Interrupt Enable is set.

SIZE A	CODE SP	NUMBER LC8-E-1	REV
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ENGINEERING SPECIFICATION



CONTINUATION SHEET

TITLE LC8-E (M8329)

- 6YY6 TLS Load Teleprinter Sequence
The teleprinter flag is cleared and AC bits 5 through 11 are transferred to the output buffer. Receiver Strobe is set. At Time State One (TS1) of the following instruction, the character is transferred to the LA30 teleprinter.

With the previous instruction the return of the signal DEMAND from the LA30 sets the teleprinter flag enabling the transfer of the next character.

In the instructions XX and YY are the device code. The standard codes are 03g and 04g for the LA30 keyboard/teleprinter respectively. These can be changed to allow several LA30's to be installed on one PDP8/E.

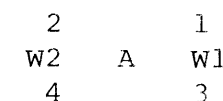
- * Cleared by initialize or CAF
- ** Set by initialize or CAF

VI Device Codes

All input/output devices on the PDP8/E have device codes which must be unique to that device. No two devices can have the same device codes. An example is 6036 where 03 would be the device code.

On the LC8-E, split lugs are provided so that any device codes between 00g and 77g can be selected separately as the transmitter and receiver device codes. Since some of these codes may be pre-assigned to other devices, a careful check should be made before any device codes are assigned.

Across the bottom edge of the M8329 are three groups of four split lugs. These determine the device codes. Each group of split lugs has an alphabetic designation (A-F), and each lug within a group is numbered (1-4). A model layout is shown below.



Machine inserted jumpers, W1 to W12, are the factory wired

SIZE A	CODE SP	NUMBER LC8-E-1	REV
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ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE LC8-E (M8329)

device codes 03 and 04 for the RECEIVER and TRANSMITTER respectively. REMEMBER to cut these jumpers before adding any jumpers to change device codes.

The strappings for RECEIVER device codes are given in the following list.

Code	Group A	Group B	Group C	Group D	Group E	Group F
00	1-3	1-3	1-3	3-4	4-2	4-2
01	1-3	1-3	1-3	3-4	4-2	1-2
02	1-3	1-3	1-3	3-4	1-2	4-2
03	1-3	1-3	1-3	3-4	1-2	1-2
04	1-3	1-3	1-3	1-4	4-2	4-2
05	1-3	1-3	1-3	1-4	4-2	1-2
06	1-3	1-3	1-3	1-4	1-2	4-2
07	1-3	1-3	1-3	1-4	1-2	1-2
10	1-3	1-3	4-3	3-4	4-2	4-2
11	1-3	1-3	4-3	3-4	4-2	1-2
12	1-3	1-3	4-3	3-4	1-2	4-2
13	1-3	1-3	4-3	3-4	1-2	1-2
14	1-3	1-3	4-3	1-4	4-2	4-2
15	1-3	1-3	4-3	1-4	4-2	1-2
16	1-3	1-3	4-3	1-4	1-2	4-2
17	1-3	1-3	4-3	1-4	1-2	1-2
20	1-3	4-3	1-3	3-4	4-2	4-2
21	1-3	4-3	1-3	3-4	4-2	1-2
22	1-3	4-3	1-3	3-4	1-2	4-2
23	1-3	4-3	1-3	3-4	1-2	1-2
24	1-3	4-3	1-3	1-4	4-2	4-2
25	1-3	4-3	1-3	1-4	4-2	1-2
26	1-3	4-3	1-3	1-4	1-2	4-2
27	1-3	4-3	1-3	1-4	1-2	1-2
30	1-3	4-3	4-3	3-4	4-2	4-2
31	1-3	4-3	4-3	3-4	4-2	1-2
32	1-3	4-3	4-3	3-4	1-2	4-2
33	1-3	4-3	4-3	3-4	1-2	1-2
34	1-3	4-3	4-3	1-4	4-2	4-2
35	1-3	4-3	4-3	1-4	4-2	1-2

SIZE **A** CODE SP NUMBER LC8-E-1 REV

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE LC8-E (M8329)

Code	Group A	Group B	Group C	Group D	Group E	Group F
36	1-3	4-3	4-3	1-4	1-2	4-2
37	1-3	4-3	4-3	1-4	1-2	1-2
40	4-3	1-3	1-3	3-4	4-2	4-2
41	4-3	1-3	1-3	3-4	4-2	1-2
42	4-3	1-3	1-3	3-4	1-2	4-2
43	4-3	1-3	1-3	3-4	1-2	1-2
44	4-3	1-3	1-3	1-4	4-2	4-2
45	4-3	1-3	1-3	1-4	4-2	1-2
46	4-3	1-3	1-3	1-4	1-2	4-2
47	4-3	1-3	1-3	1-4	1-2	1-2
50	4-3	1-3	4-3	3-4	4-2	4-2
51	4-3	1-3	4-3	3-4	4-2	1-2
52	4-3	1-3	4-3	3-4	1-2	4-2
53	4-3	1-3	4-3	3-4	1-2	1-2
54	4-3	1-3	4-3	1-4	4-2	4-2
55	4-3	1-3	4-3	1-4	4-2	1-2
56	4-3	1-3	4-3	1-4	1-2	4-2
57	4-3	1-3	4-3	1-4	1-2	1-2
60	4-3	4-3	1-3	3-4	4-2	4-2
61	4-3	4-3	1-3	3-4	4-2	1-2
62	4-3	4-3	1-3	3-4	1-2	4-2
63	4-3	4-3	1-3	3-4	1-2	1-2
64	4-3	4-3	1-3	1-4	4-2	4-2
65	4-3	4-3	1-3	1-4	4-2	1-2
66	4-3	4-3	1-3	1-4	1-2	4-2
67	4-3	4-3	1-3	1-4	1-2	1-2
70	4-3	4-3	4-3	3-4	4-2	4-2
71	4-3	4-3	4-3	3-4	4-2	1-2
72	4-3	4-3	4-3	3-4	1-2	4-2
73	4-3	4-3	4-3	3-4	1-2	1-2
74	4-3	4-3	4-3	1-4	4-2	4-2
75	4-3	4-3	4-3	1-4	4-2	1-2
76	4-3	4-3	4-3	1-4	1-2	4-2
77	4-3	4-3	4-3	1-4	1-2	1-2

SIZE **A** CODE SP NUMBER LC8-E-1 REV

ENGINEERING SPECIFICATION

digital

CONTINUATION SHEET

TITLE LC8-E (M8329)

The strappings for TRANSMITTER device codes are given in the following list.

Code	Group A	Group B	Group C	Group D	Group E	Group F
00	1-2	1-2	1-2	3-2	4-3	4-3
01	1-2	1-2	1-2	3-2	4-3	1-3
02	1-2	1-2	1-2	3-2	1-3	4-3
03	1-2	1-2	1-2	3-2	1-3	1-3
04	1-2	1-2	1-2	1-2	4-3	4-3
05	1-2	1-2	1-2	1-2	4-3	1-3
06	1-2	1-2	1-2	1-2	1-3	4-3
07	1-2	1-2	1-2	1-2	1-3	1-3
10	1-2	1-2	4-2	3-2	4-3	4-3
11	1-2	1-2	4-2	3-2	4-3	1-3
12	1-2	1-2	4-2	3-2	1-3	4-3
13	1-2	1-2	4-2	3-2	1-3	1-3
14	1-2	1-2	4-2	1-2	4-3	4-3
15	1-2	1-2	4-2	1-2	4-3	1-3
16	1-2	1-2	4-2	1-2	1-3	4-3
17	1-2	1-2	4-2	1-2	1-3	1-3
20	1-2	4-2	1-2	3-2	4-3	4-3
21	1-2	4-2	1-2	3-2	4-3	1-3
22	1-2	4-2	1-2	3-2	1-3	4-3
23	1-2	4-2	1-2	3-2	1-3	1-3
24	1-2	4-2	1-2	1-2	4-3	4-3
25	1-2	4-2	1-2	1-2	4-3	1-3
26	1-2	4-2	1-2	1-2	1-3	4-3
27	1-2	4-2	1-2	1-2	1-3	1-3
30	1-2	4-2	4-2	3-2	4-3	4-3
31	1-2	4-2	4-2	3-2	4-3	1-3
32	1-2	4-2	4-2	3-2	1-3	4-3
33	1-2	4-2	4-2	3-2	1-3	1-3
34	1-2	4-2	4-2	1-2	4-3	4-3
35	1-2	4-2	4-2	1-2	4-3	1-3
36	1-2	4-2	4-2	1-2	1-3	4-3
37	1-2	4-2	4-2	1-2	1-3	1-3

SIZE **A** CODE SP NUMBER LC8-E-1 REV

ENGINEERING SPECIFICATION

digital

CONTINUATION SHEET

TITLE LC8-E (M8329)

Code	Group A	Group B	Group C	Group D	Group E	Group F
40	4-2	1-2	1-2	3-2	4-3	4-3
41	4-2	1-2	1-2	3-2	4-3	1-3
42	4-2	1-2	1-2	3-2	1-3	4-3
43	4-2	1-2	1-2	3-2	1-3	1-3
44	4-2	1-2	1-2	1-2	4-3	4-3
45	4-2	1-2	1-2	1-2	4-3	1-3
46	4-2	1-2	1-2	1-2	1-3	4-3
47	4-2	1-2	1-2	1-2	1-3	1-3
50	4-2	1-2	4-2	3-2	4-3	4-3
51	4-2	1-2	4-2	3-2	4-3	1-3
52	4-2	1-2	4-2	3-2	1-3	4-3
53	4-2	1-2	4-2	3-2	1-3	1-3
54	4-2	1-2	4-2	1-2	4-3	4-3
55	4-2	1-2	4-2	1-2	4-3	1-3
56	4-2	1-2	4-2	1-2	1-3	4-3
57	4-2	1-2	4-2	1-2	1-3	1-3
60	4-2	4-2	1-2	3-2	4-3	4-3
61	4-2	4-2	1-2	3-2	4-3	1-3
62	4-2	4-2	1-2	3-2	1-3	4-3
63	4-2	4-2	1-2	3-2	1-3	1-2
64	4-2	4-2	1-2	1-2	4-3	4-3
65	4-2	4-2	1-2	1-2	4-3	1-3
66	4-2	4-2	1-2	1-2	1-3	4-3
67	4-2	4-2	1-2	1-2	1-3	1-3
70	4-2	4-2	4-2	3-2	4-3	4-3
71	4-2	4-2	4-2	3-2	4-3	1-3
72	4-2	4-2	4-2	3-2	1-3	4-3
73	4-2	4-2	4-2	3-2	1-3	1-3
74	4-2	4-2	4-2	1-2	4-3	4-3
75	4-2	4-2	4-2	1-2	4-3	1-3
76	4-2	4-2	4-2	1-2	1-3	4-3
77	4-2	4-2	4-2	1-2	1-3	1-3

SIZE **A** CODE SP NUMBER LC8-E-1 REV

